

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned  
Examiner: To Be Assigned

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U.S. PTO  
J1003

In re PATENT APPLICATION of

Applicant(s) : Tadashi YAMAGUCHI

Appln. No. : To Be Assigned

Filed : Herewith

For : SEMICONDUCTOR DEVICE

Atty. Dkt. : KAN 137

#3/IDS  
3/9/02  
INFORMATION  
DISCLOSURE  
STATEMENT  
V.Varnall

Commissioner for Patents  
Washington, D.C. 20231

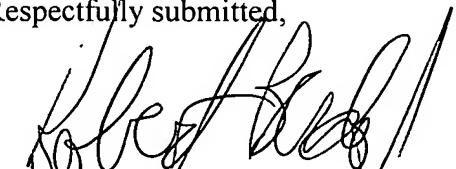
Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of 2 published Japanese-language patents with English-language abstracts. The relevance of these documents can be gleaned from the English-language Abstracts. The published patents are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents are respectfully requested.

Respectfully submitted,

  
Robert H. Berdo, Jr. (Reg. No. 38,075)  
Customer No. 23995

December 18, 2001

Date

RHB:tlc